Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	91	differential adj logic adj gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:51
L2	8	differential adj logic adj gate.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:51
L3	4029	pass adj device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:51
L4	6	pass adj device same pullup	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:51
<b>L5</b>	13	differential adj input adj "AND" adj gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:52
L6	11408	"AND" adj gate.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:52
<b>L7</b>	139	"AND" adj gate.clm. same differential.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR The second se	ON	2005/10/21 08:52
L8	0	"AND" adj gate.clm. same differential.clm. and pass adj device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:52

L9	16	"AND" adj gate.clm. same differential.clm. and complement	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:53
L10	15887	"AND" adj gate.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:53
L11	473	"AND" adj gate.ti. and differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:53
L12	4	"AND" adj gate.ti. and differential and complement	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:53
L13	3	"AND" adj gate.ti. and differential. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:54
L14	1	"AND" adj gate.ti. and "NAND".clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:54
L15	442	"AND" adj gate.ti. and transistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:54
<b>L16</b>	11	"AND" adj gate.ti. and transistors. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/21 08:54

## **DETAILED ACTION**

The indicated allowability of claims 13 and 18 is withdrawn in view of the newly discovered reference(s) to Tamedai (US 5,442,614) in view of Endsley et al (US 6,005,613). Rejections based on the newly cited reference(s) follow.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 13 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tamegai (US 5,442,614)in view of Endsley et al (US 6,005,613).

## [claims 13 & 18]

In regard to Claims 13 and 18, Tamegai discloses an image recording/reproducing method and apparatus for recording image information on a recording medium and for reproducing the image information thus recorded, comprising the steps of:

- At a time of recording image information on the recording medium,
   executing (Column 2 Lines 45+) at least one sub step of:
  - Setting so as not to execute a verify mode (Figure 5 step s3 shows the verifying of the recorded data as further explained in Column 6 Lines 17-24):

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- Setting so as not to execute a re-reading operation (Figure 5 shows the executing of the re-reading operation from s4 wherein the data has not been written and thereby performs a re-read), and
- Setting so as not to execute an automatic defect exchange function
   (Column 1 Lines 23-30 describes the automatic defect exchange function); however, fails to disclose
  - When image information thus recorded on the recording medium in said executing step is read and displayed on a monitor for each frame, preventing the frame of the image information with a read error from being read again when the frame of the image information having been read just before the frame error.

Endsley discloses a recording apparatus wherein image information is displayed when an error occurs. The image being displayed is the "old" frame that will be repeated instead of the current frame with the error, as described in Column 9 Lines 25-40. The displaying of the previous frame without error allows for the system to process the current error and alert user of the error. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use the recording data and information apparatus, as disclosed by Tamegai, and incorporate a system that shows the previous frame that does not have an error, as disclosed by Endsley.

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## **Contact Information**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jamie Vent whose telephone number is 571-272-7384.

The examiner can normally be reached on 7:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Groody can be reached on 571-272-7950. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jamie Vent

James J. Groody
Supervisory Patent Examiner
Art Unit 262-2016

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